**Core Specifications**

<table>
<thead>
<tr>
<th>Manufacturer</th>
<th>Family</th>
<th>Data Width</th>
<th>Speed</th>
<th>Price</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Agere Systems</strong></td>
<td>DSP1500xxx</td>
<td>16 bits</td>
<td>110 256 MHz</td>
<td>40–110 Dual MAC architecture</td>
</tr>
<tr>
<td><strong>Analog Devices</strong></td>
<td>ADSP-2116x</td>
<td>16 bits</td>
<td>320 MHz</td>
<td>3–49</td>
</tr>
<tr>
<td><strong>Hitachi</strong></td>
<td>SH775x (SH-4)</td>
<td>32 bits</td>
<td>200 MHz</td>
<td>250–600</td>
</tr>
</tbody>
</table>
| **Infineras** | TMS320C54xx | 16 bits | 400 MHz | 5–10 \$
| **LSI Logic** | LST15000DSP301 | 16 bits | 390 MHz | 5–390 Dual DSP architecture based on 5400 core (see below) |

**Notes**

1. Chip cost based on factory family member; Core cost worst-case chip cost.
2. The BDTI benchmark scores are generic 32-bit scores that can be scaled upward to larger codes using the following formulas: score doubles for each 32-bit increase in code size.
3. BDTI simMark™ scores are produced for each member of the factory.
4. For one core (the MSH 110 contains four cores).
5. For one core (the MSH 110 contains four cores).
6. The CoreCom scores were developed by Materials and Agere's Joint Design Center.
7. SuperH is an independent licensing company formed by Hitachi and STMicroelectronics.

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